

WEST Search History

[Hide Items](#) [Restore](#) [Clear](#) [Cancel](#)

DATE: Wednesday, September 27, 2006

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L22	l20 and l13	3
<input type="checkbox"/>	L21	l19 and L20	3
<input type="checkbox"/>	L20	l4 and ((logical logic) with composition)	62
<input type="checkbox"/>	L19	l13 and L18	6
<input type="checkbox"/>	L18	l4 and ((logical logic) with specification)	226
<input type="checkbox"/>	L17	L16 and buffering	4
<input type="checkbox"/>	L16	l13 and L15	23
<input type="checkbox"/>	L15	l4 and ((optimize optimization optimizing) with (logic logical))	193
<input type="checkbox"/>	L14	L13 and buffer\$	27
<input type="checkbox"/>	L13	l4 and (register\$ near10 (library or libraries))	40
<input type="checkbox"/>	L12	L11 and (library or libraries)	89
<input type="checkbox"/>	L11	l4 and l8	257
<input type="checkbox"/>	L10	"pass transistor" same ((inactive or "not active") near2 simultaneously)	0
<input type="checkbox"/>	L9	l7 and L8	19
<input type="checkbox"/>	L8	716/\$[ccls]	10801
<input type="checkbox"/>	L7	"pass transistor" same (exclusive or ((inactive or "not active") near2 simultaneously))	177
<input type="checkbox"/>	L6	l3 and l4	223
<input type="checkbox"/>	L5	l3 or l4	15834
<input type="checkbox"/>	L4	"pass transistor"	7707
<input type="checkbox"/>	L3	"path transistor"	8350
<i>DB=USPT,PGPB; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L2	('5020013' '5712792' '5973507' '6232799' '6288568')![pn]	5
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L1	(6720797 5712792)[pn]	2

END OF SEARCH HISTORY